



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Lehmann, *et al.* Attorney Docket: 00P09113 US
Filed: December 28, 2000 Examiner: Unknown
Serial No.: 09/751,474 Art Unit: 2811
Title: Area Efficient Stacking of Antifuses in Semiconductor Device

Mail Stop Amendment
Commissioner for Patents
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INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant wishes to bring to the attention of the U.S. Patent and Trademark Office the information noted on the enclosed Form PTO/SB/08a, which may be considered material to the examination of the above-identified application. Pursuant to 37 CFR 1.98(a)(2)(i), as amended, copies of U.S. Patents cited are not being submitted. However, Applicant has included a copy of the international application.

This Information Disclosure Statement is submitted under 37 C.F.R. §1.97(c) together with a \$180.00 fee under 37 C.F.R. §1.17(p) after the C.F.R. §1.97(b) time period, but before final action or notice of allowance, whichever occurs first.

Please charge the required fee of \$180.00 and any additional amount, or credit any overpayment to Deposit Acct. No 50-1065 of the below mentioned firm.

Respectfully submitted,


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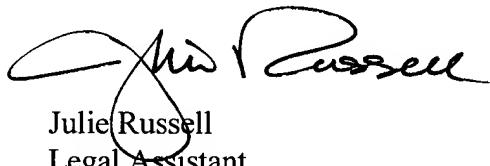
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Information Disclosure Statement (1 original and 1 copy – 2 pages)
Form PTO/SB/08A (1 page), 8 references cited, 1 included herewith
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Respectfully submitted,



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